

wherein the emulation instructions bypass the at least one other device and are executed by the one device.

34. (original) The emulator of claim 33, wherein said translation module comprises a FPGA disposed within said emulator connector.

Remarks

Applicant believes that this amendment places the subject application in better condition for allowance and in so doing introduces no new issues. Therefore, entry of this Amendment, reconsideration of the application, and allowance of all claims pending herein is respectfully requested.

Claims 1-34 were originally presented in the subject application. By the foregoing amendment, the claims 3-7, 9, 10, 13, 14, 16, 17, 22, 23 and 29-31 have been amended to more particularly point out and distinctly claim the inventive material of the subject invention. In addition, claims 1 and 2 are canceled without prejudice or disclaimer. Claims 3-34 remain in this case.

Initially, the Applicant wishes to thank the Examiner for the indication of allowability given original claims 6 and 9 if rewritten in independent form, and that claims 32-34 are allowed over the prior art of record. In response, submitted herewith is the requested rewrite of claims 6 and 9. Claims 3-5, 7, 8 and 10-22 now depend directly or indirectly from claims 6 or 9, claims 3-22 are now believed to be in allowable form. Claims 23-28 now depend directly or indirectly from allowed claim 33, and are thus believed to be similarly allowable. Claims 29-31 have been amended to include the salient features of allowable claim 9, and are thus also believed to be in allowable form.

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CONCLUSION

The Applicant therefore submits that all of the stated grounds of rejection have been properly addressed or rendered moot.

This application is now believed to be in condition for allowance, and such action at an early date is respectfully requested. However, if any matters remain unresolved, the Examiner is encouraged to contact the undersigned by telephone.

Respectfully submitted,



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